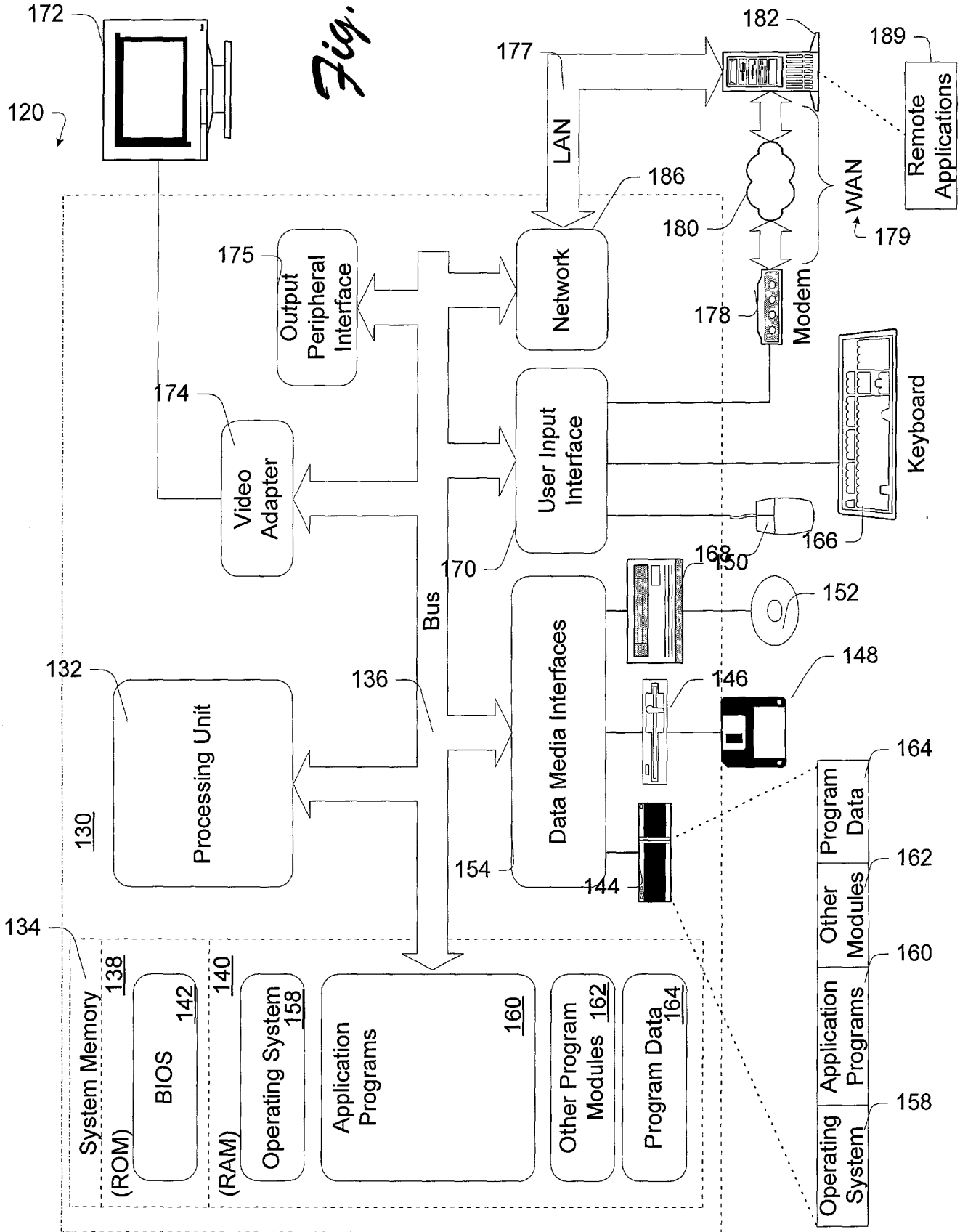


Fig. 1



SECRET - TOP SECRET

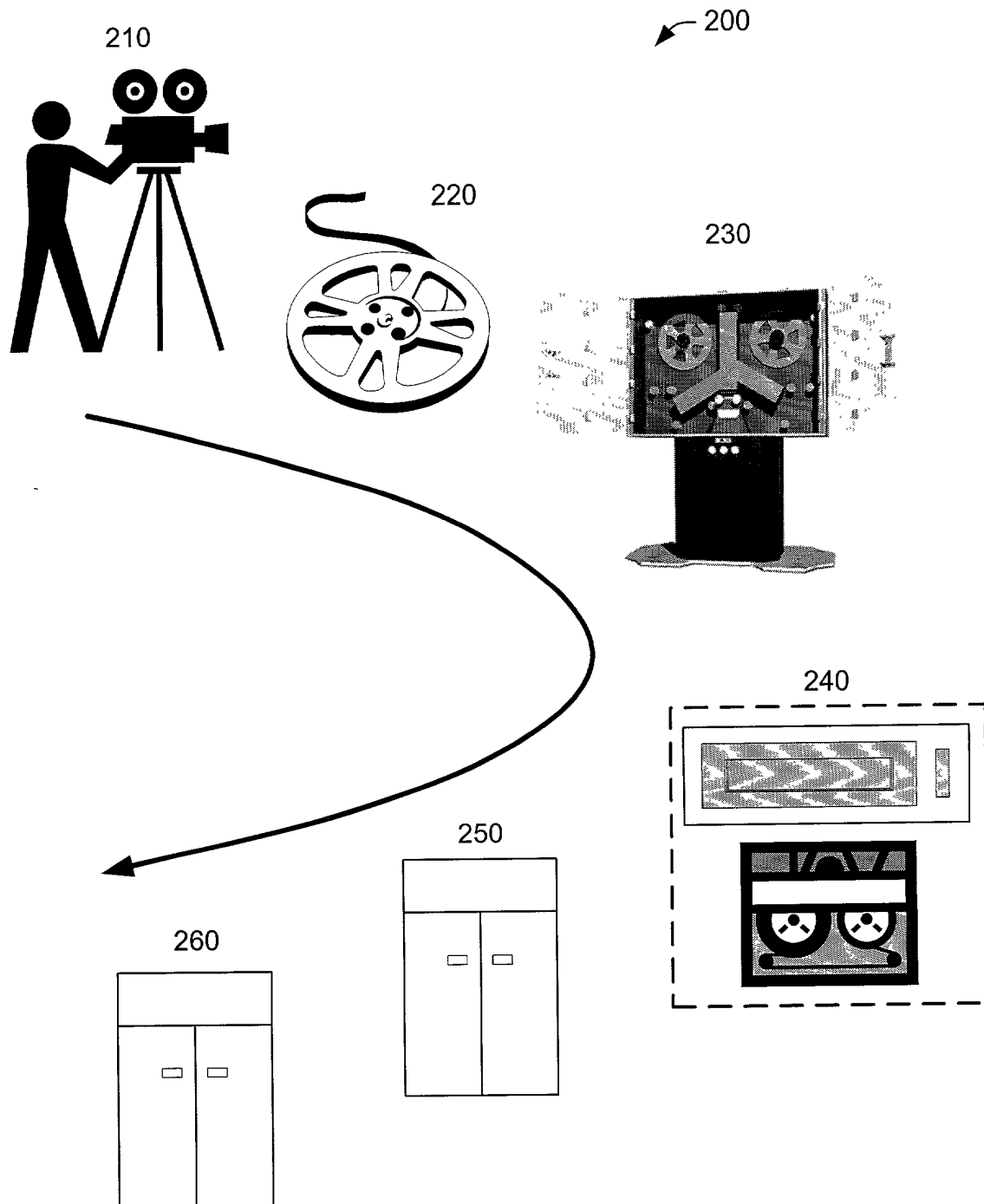


Fig. 2

EXEMPLARY CONVERSION PROCESS

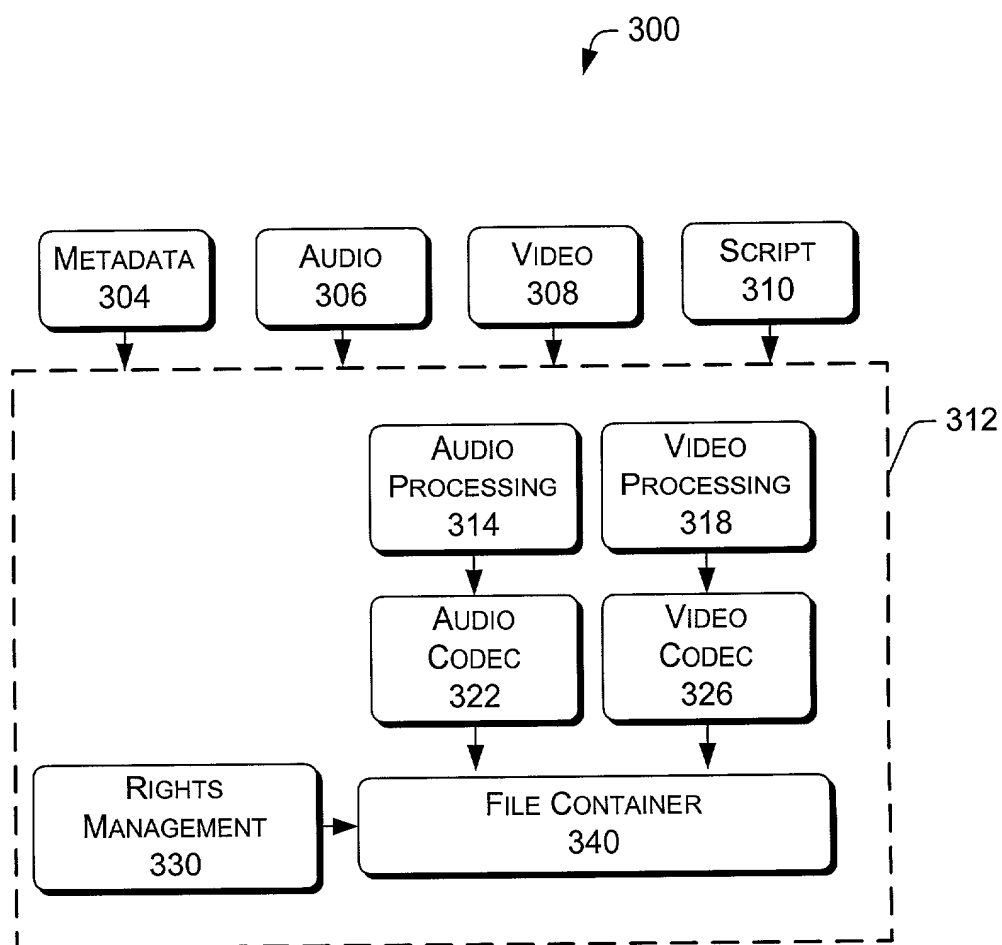


Fig. 3

EXEMPLARY COMPRESSION/DECOMPRESSION
PROCESS

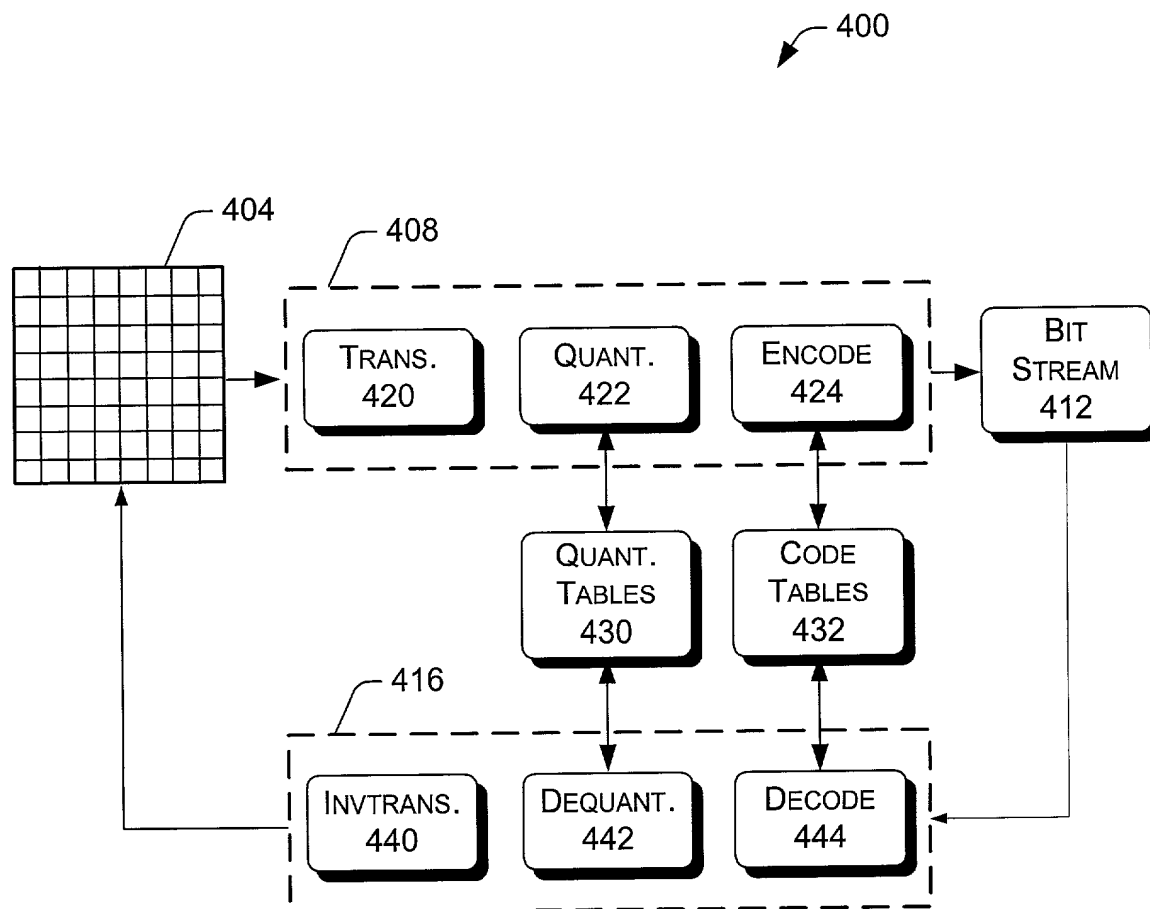


Fig. 4

EXEMPLARY METHOD

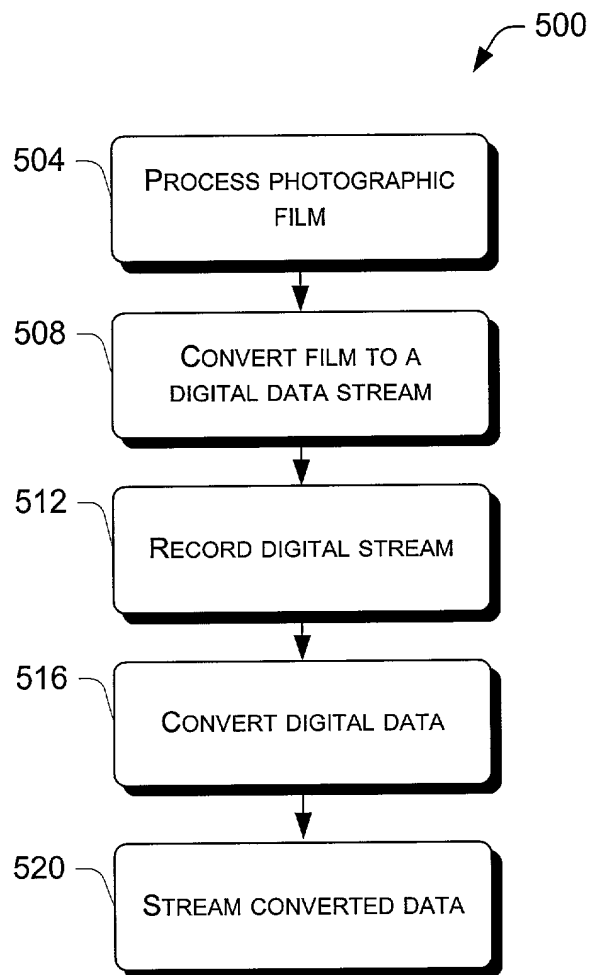


Fig. 5

EXEMPLARY DEVICE AND/OR SYSTEM

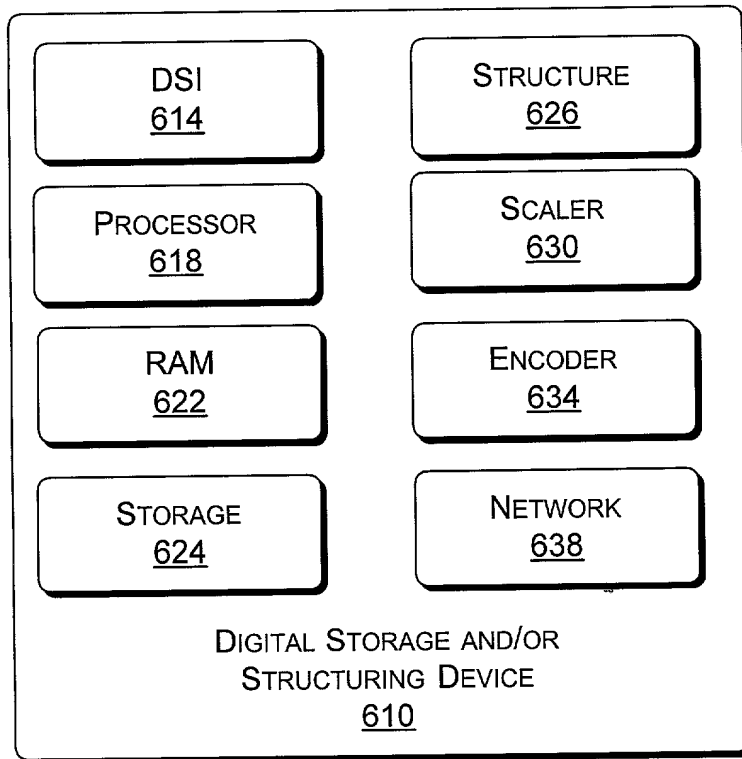


Fig. 6

EXEMPLARY METHOD

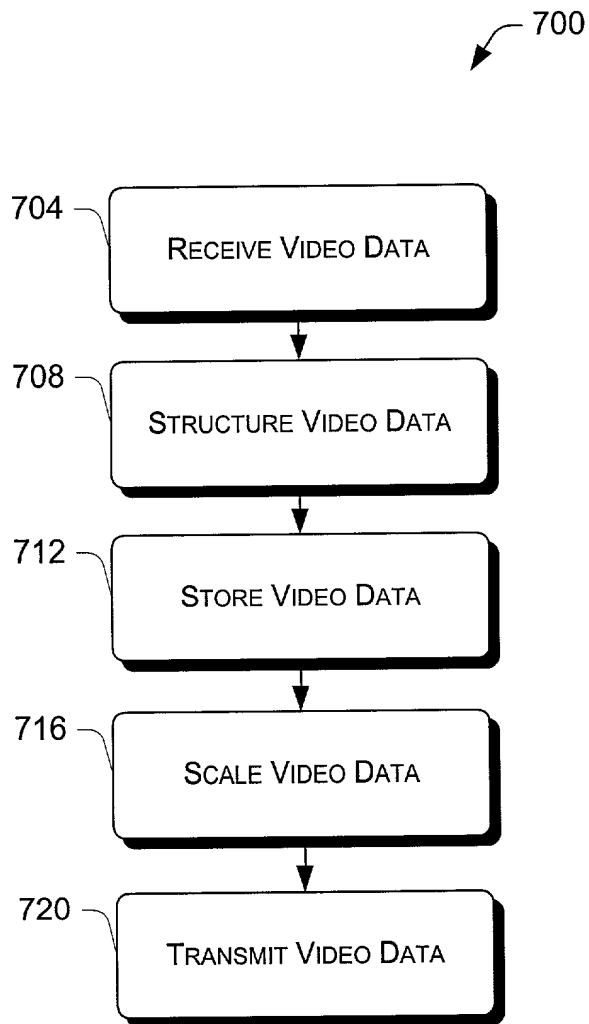


Fig. 7

EXEMPLARY METHOD

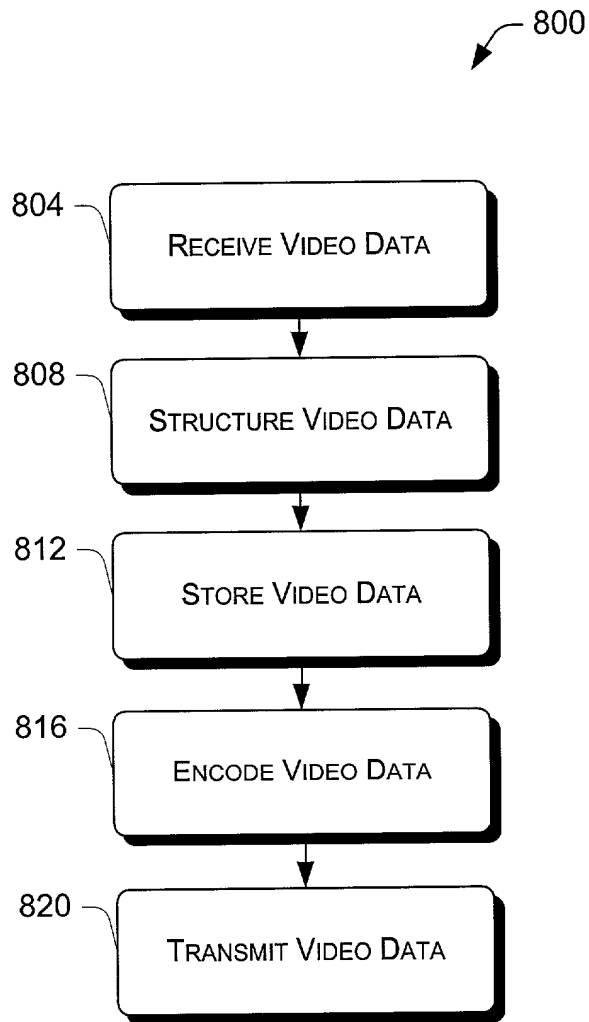


Fig. 8

EXEMPLARY METHOD

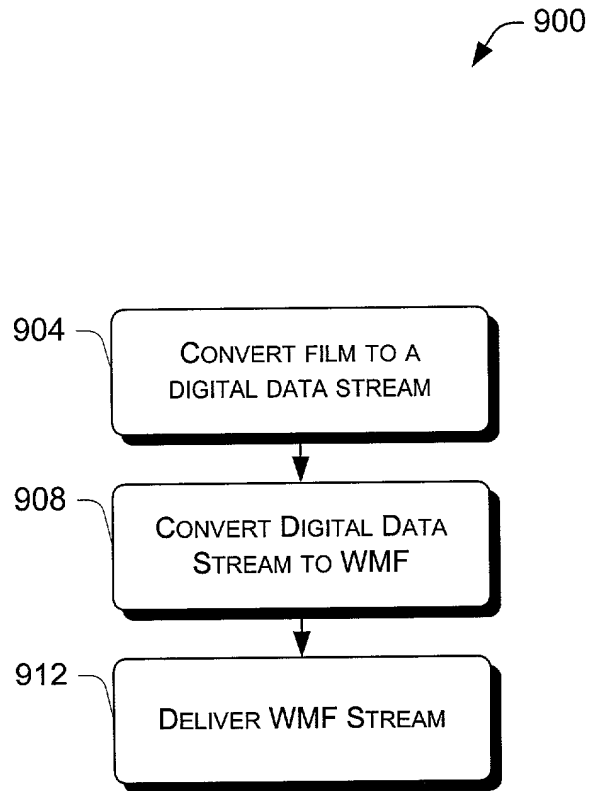


Fig. 9

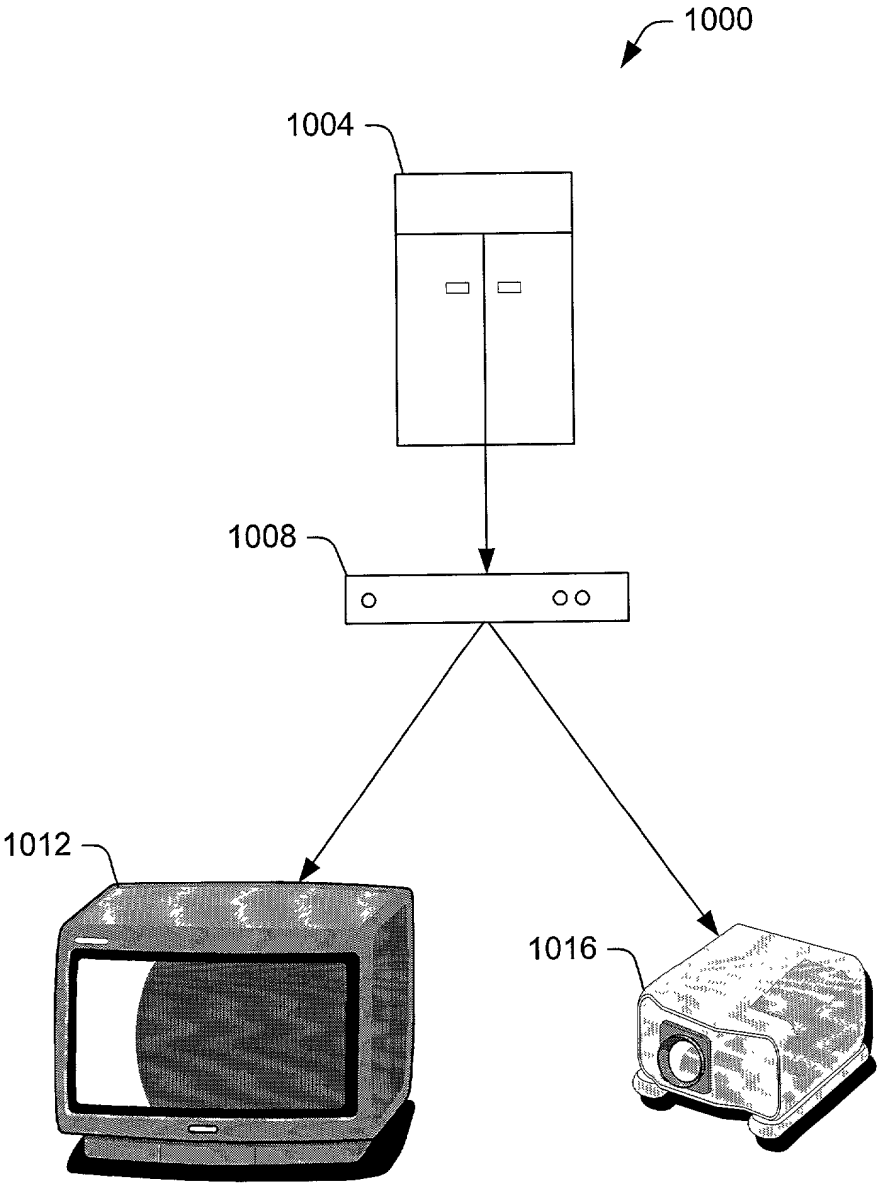


Fig. 10

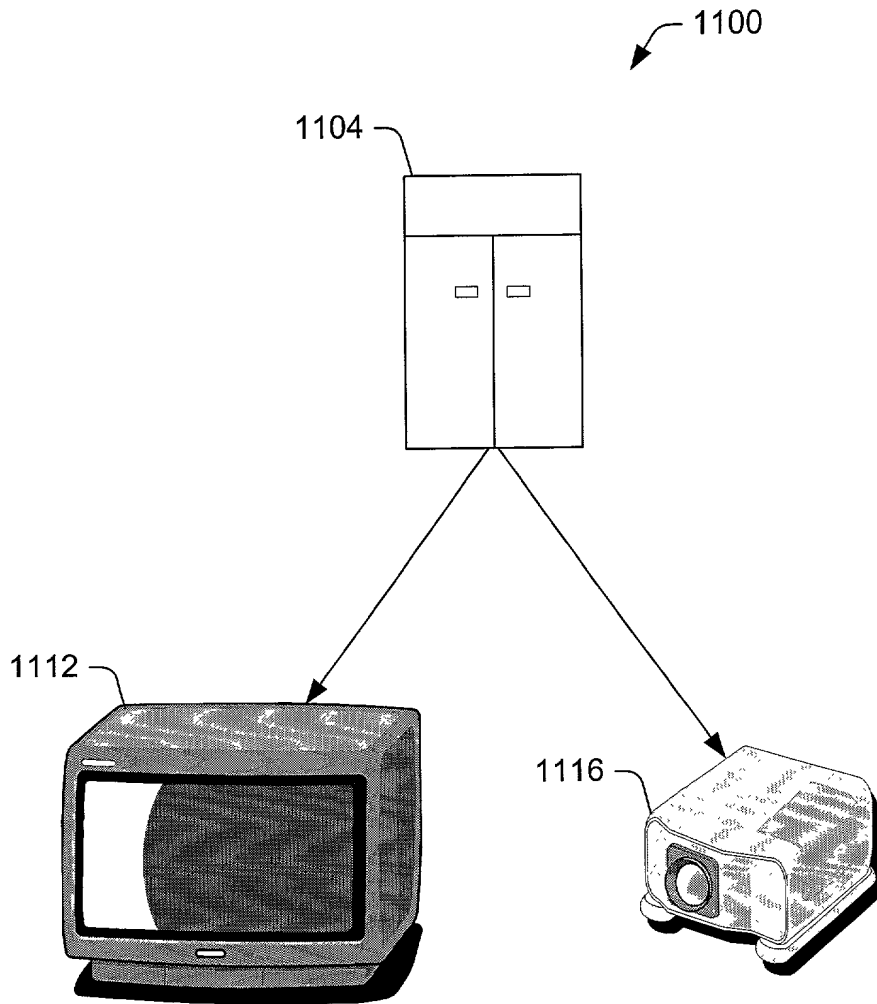


Fig. 11

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EXEMPLARY METHOD 1200

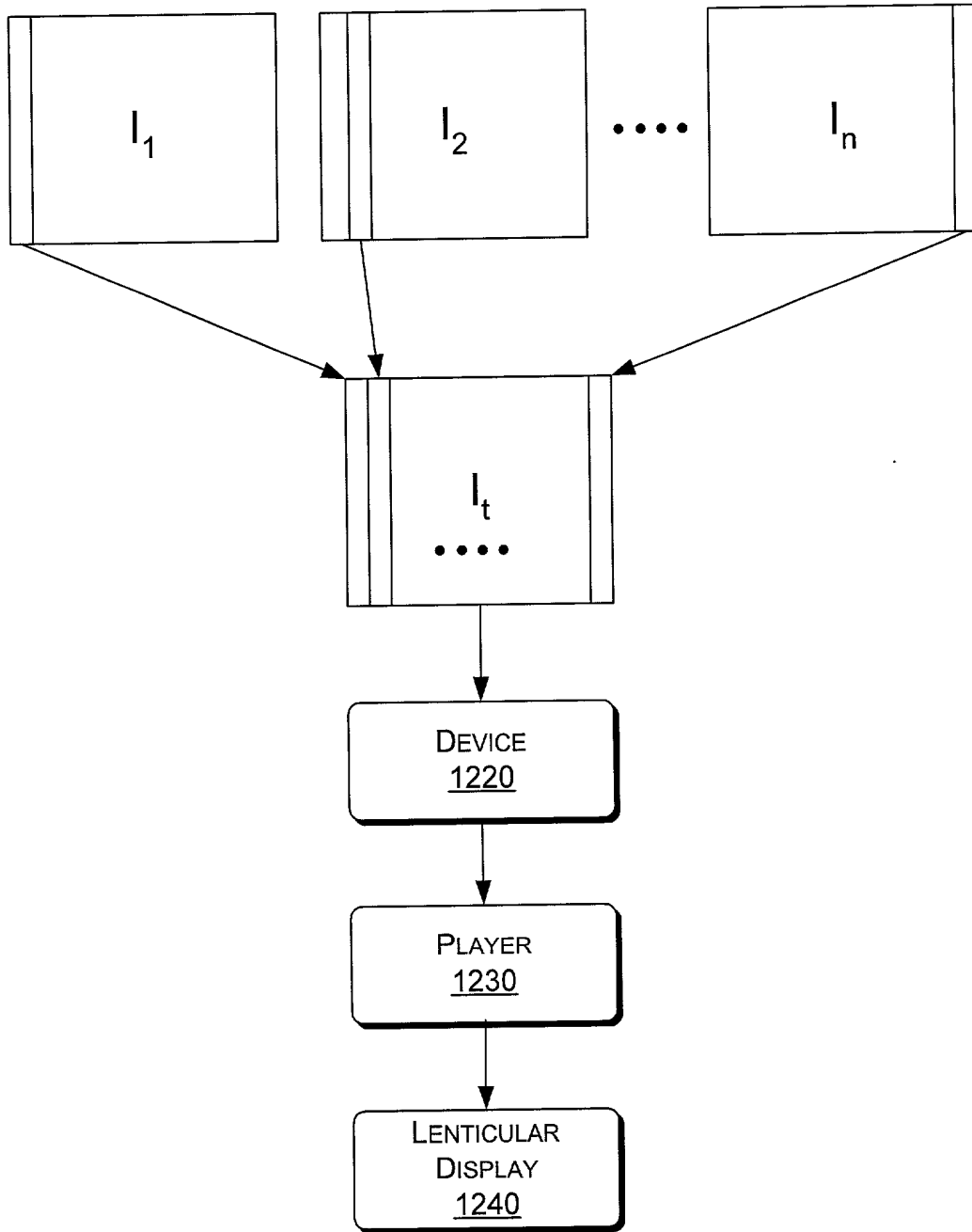


Fig. 12

EXEMPLARY METHOD

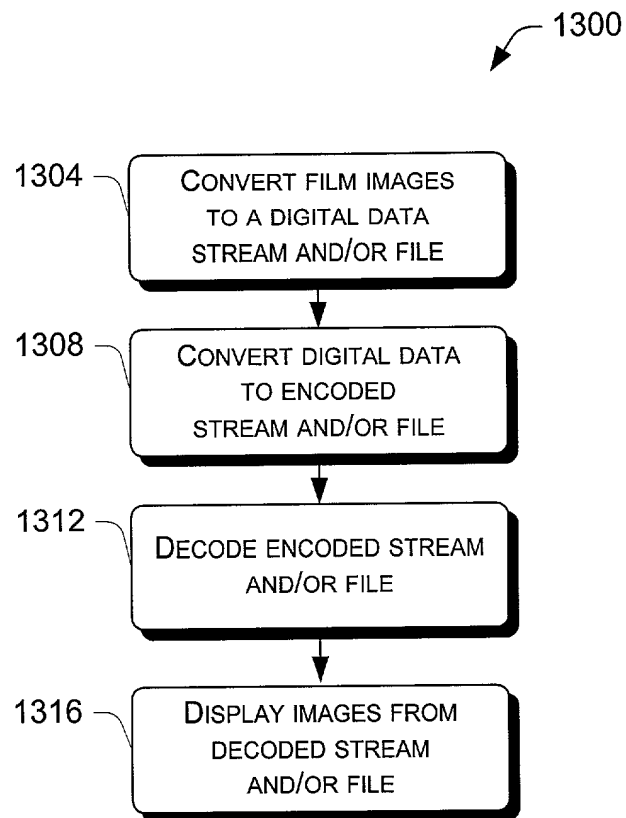


Fig. 13

SINGLE PROCESSOR COMPUTERS

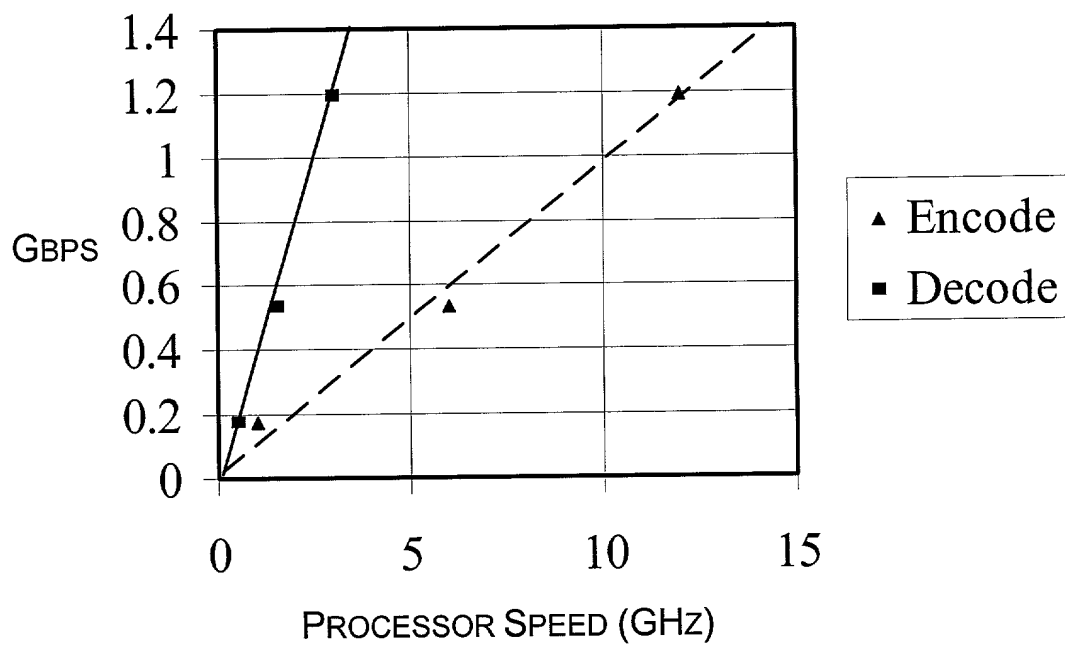


Fig. 14